



SHEET 1 OF 1

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

(PTO-1449)

ATTY. DOCKET NO.
050099-0246SERIAL NO.
10/797,199APPLICANT
Tatsuya KUNIKIYO, et al.FILING DATE
March 11, 2004GROUP
2824

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
at		US 5,793,697	8-11-1998	Scheuerlein	265/230.07
at		US 5,852,574	12-22-1998	Naji	365/158
		US			
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code ¹ -Number &-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
at		EP 1 094 467 A2	4-25-2001	MOTOROLA, INC	GIAC 1A/15	X
at		JP 63-044398	2-25-1988		GIAC 1A/14	X
at		JP 05-095055	4-16-1993		HOLL 23/28	X
at		JP 11-289186	10-19-1999		HOSK 9/00	X

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
at		SCHEUERLEIN, R.E.: "Magnetoresistive IC memory Limitations and Architecture Implications, IEEE Nonvolatile Memory Technology Conference, June 22-24, 1998, 47-50
at		ITOH, K. et al.: "Reviews and Prospects of High-Density RAM Technology" International Semiconductor Conference Proceedings, Vol. 1, October 10-14, 2000, 13-22
at		SCHEUERLEIN, R. et al.: "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell", IEEE International Solid-State Circuits Conference, February 7-9, 2000, 128-129
at		DURLAM, M. et al., "Nonvolatile RAM based on Magnetic Tunnel Junction Elements", IEEE International Solid-State Circuits Conference, February 7-9, 2000, 130-131
at		JOHNSON, M.: "Magnetoelectronic memories last and last", IEEE Spectrum Vol. 37 No. 2 February 2000, 33-40

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.